

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : US 7,730,515 B1  
Issue Date : June 1, 2010  
Inventors : Andrew MacCormack et al.

Docket No. : 858063.435  
Date : November 22, 2010

Mail Stop Certificate of Correction  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents:

A certificate of correction is respectfully requested in the above-identified patent.  
The following errors have been made:

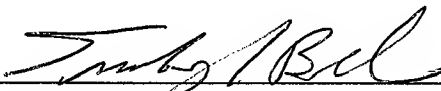
In the claims, columns 19 and 20, issued claim 23 read as, "A receiver for processing a packetized digital data stream, the receiver comprising: an input module to receive and process a data packet; a memory; a receiver processor to control storage of desired packet identifiers and associated control information in the memory; and a transport controller having a transport processor to extract a packet identifier from a packet in the input module and a search engine to search the memory for a match of the extracted packet identifier to a desired packet identifier stored in the memory, wherein responsive to a match the transport processor retrieves from the memory control information associated with the desired packet identifier stored in the memory and controls processing of the received data packet by the input module based on the retrieved control information; wherein the transport processor generates a control signal to control processing of a packet by the input module based on associated control information retrieved from the memory; wherein the input module passes a data payload to the transport controller in response to the control signal." which is incorrect. As set forth in the Amendment filed September 15, 2009 with the United States Patent and Trademark Office, the correct claim should read, --The receiver of claim 20 wherein the input module passes a data payload to the

transport controller in response to the control signal.-- Issued claim 23 appears as claim 43 during prosecution.

In the claims, column 20, issued claim 24 reads as, "A receiver for processing a packetized digital data stream, the receiver comprising: an input module to receive and process a data packet; a memory; a receiver processor to control storage of desired packet identifiers and associated control information in the memory; and a transport controller having a transport processor to extract a packet identifier from a packet in the input module and a search engine to search the memory for a match of the extracted packet identifier to a desired packet identifier stored in the memory, wherein responsive to a match the transport processor retrieves from the memory control information associated with the desired packet identifier stored in the memory and controls processing of the received data packet by the input module based on the retrieved control information; wherein the transport processor generates a control signal to control processing of a packet by the input module based on associated control information retrieved from the memory; wherein the input module passes a data payload to the transport controller in response to the control signal; wherein the transport controller reformats the data payload based on the control information and passes the reformatted data payload to the input module for output in an alternative output stream." which is incorrect. As set forth in the Amendment filed September 15, 2009 with the United States Patent and Trademark Office, the correct claim should read, --The receiver of claim 23 wherein the transport controller reformats the data payload based on the control information and passes the reformatted data payload to the input module for output in an alternative output stream.-- Claim 24 appears as claim 44 during prosecution.

Attached is the certificate of correction, which indicates the corrections to be made, by reference to the column and line numbers in the printed patent. Because the errors were made by the Patent Office, no fee is necessary. However if the errors are determined to be our errors, the Director is hereby authorized to charge payment of any fees associated with this communication to Deposit Account No. 19-1090.

Respectfully submitted,  
SEED Intellectual Property Law Group PLLC

  
\_\_\_\_\_  
Timothy L. Boller  
Registration No. 47,435

TLB:ccj

Enclosure:  
Certificate of Correction

SEED Intellectual Property Law Group PLLC  
701 Fifth Avenue, Suite 5400  
Seattle, Washington 98104-7092  
Phone: (206) 622-4900  
Fax: (206) 682-6031

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**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO : US 7,730,515 B1  
APPLICATION NO. : 09/239,907  
DATED : June 1, 2010  
INVENTORS : Andrew MacCormack et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Columns 19 & 20

Claim 23, "A receiver for processing a packetized digital data stream, the receiver comprising: an input module to receive and process a data packet; a memory; a receiver processor to control storage of desired packet identifiers and associated control information in the memory; and a transport controller having a transport processor to extract a packet identifier from a packet in the input module and a search engine to search the memory for a match of the extracted packet identifier to a desired packet identifier stored in the memory, wherein responsive to a match the transport processor retrieves from the memory control information associated with the desired packet identifier stored in the memory and controls processing of the received data packet by the input module based on the retrieved control information; wherein the transport processor generates a control signal to control processing of a packet by the input module based on associated control information retrieved from the memory; wherein the input module passes a data payload to the transport controller in response to the control signal." should read as --The receiver of claim 20 wherein the input module passes a data payload to the transport controller in response to the control signal.--.

Column 20

Claims 24, "A receiver for processing a packetized digital data stream, the receiver comprising: an input module to receive and process a data packet; a memory; a receiver processor to control storage of desired packet identifiers and associated control information in the memory; and a transport controller having a transport processor to extract a packet identifier from a packet in the input module and a search engine to search the memory for a match of the extracted packet identifier to a desired packet identifier stored in the memory, wherein responsive to a match the transport processor retrieves from the memory control information associated with the desired packet identifier stored in the memory and controls processing of the received data packet by the input module based on the retrieved control information; wherein the transport processor generates a control signal to control processing of a packet by the input module based on associated control information retrieved from the memory; wherein the input module passes a data payload to the transport controller in response to the control signal; wherein the transport controller reformats the data payload based on the control information and passes the reformatted data payload to the input module for output in an alternative output stream." should read as --The receiver of claim 23 wherein the transport controller reformats the data payload based on the control information and passes the reformatted data payload to the input module for output in an alternative output stream.--

MAILING ADDRESS OF SENDER:

Timothy L. Boller  
Seed Intellectual Property Law Group PLLC  
701 Fifth Avenue, Suite 5400  
Seattle, Washington 98104

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